



PATENT
8008-1052

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasutaka NAKASHIBA

Conf. 2273

Application No. 10/812,282

Group 2815

Filed March 30, 2004

Examiner Edgardo Ortiz

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

RESPONSE

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 19, 2005

Sir:

In response to the Official Action of April 21, 2005,
reconsideration and withdrawal of the rejection are respectfully
requested.

Remarks begin on page 2 of this paper.